FIG. 1

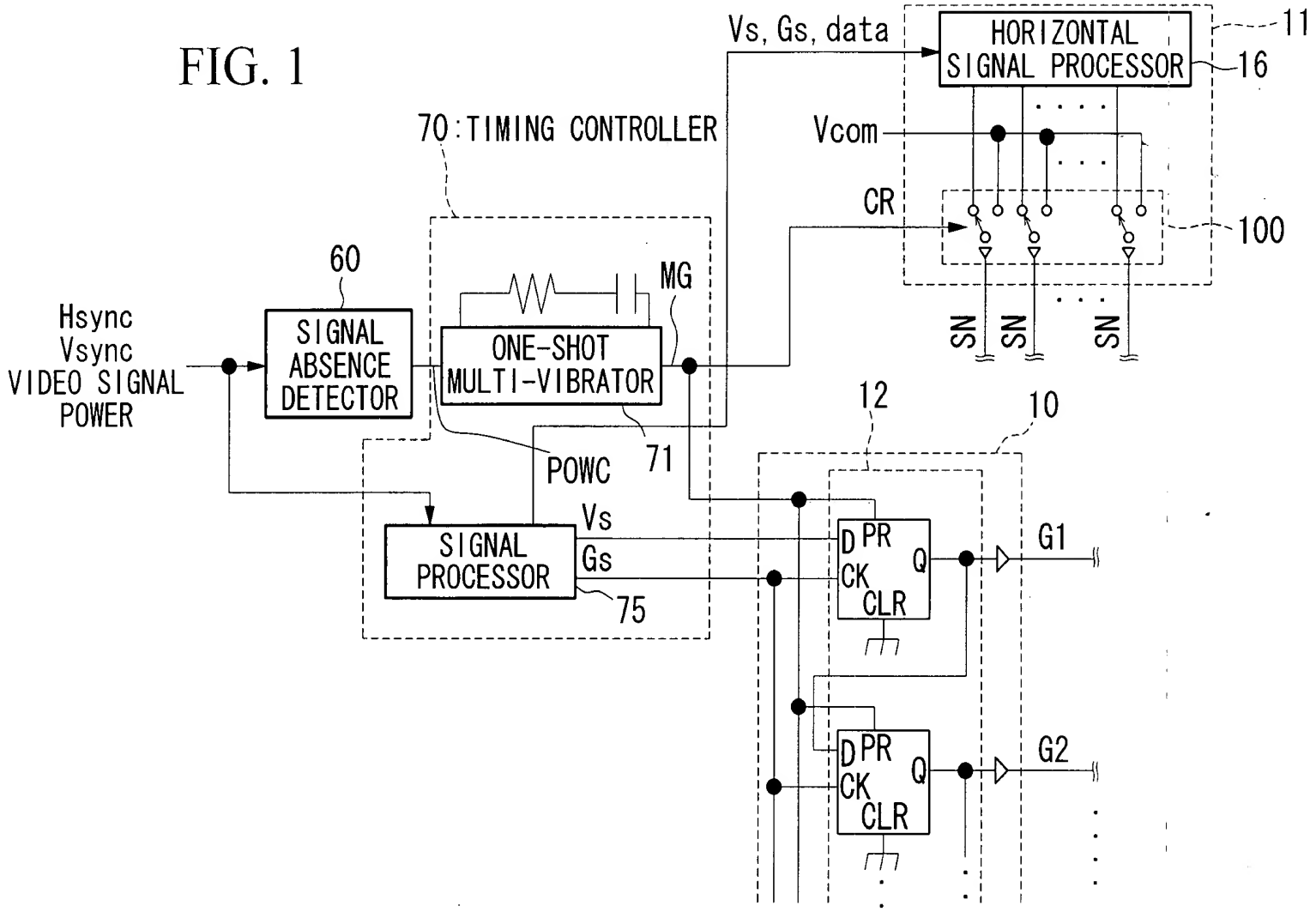
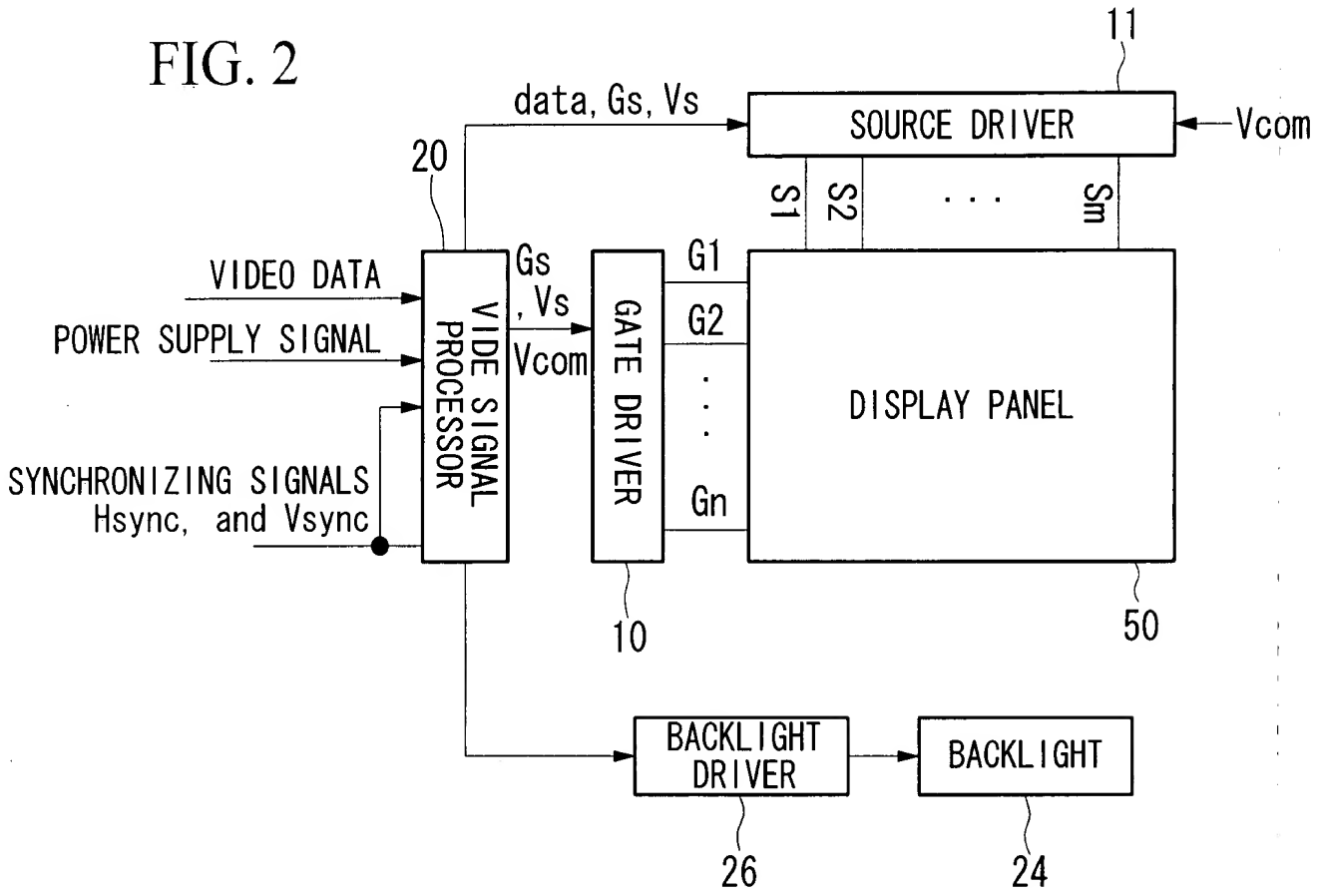

Vs, Gs, data
HORIZONTAL SIGNAL PROCESSOR
11
16
70 : TIMING CONTROLLER
Vcom
CR
100
60
MG
Hsync
Vsync
VIDEO SIGNAL
POWER
SIGNAL ABSENCE DETECTOR
ONE-SHOT MULTI-VIBRATOR
SN
SN
SN
12
10
POWC
71
Vs
Gs
SIGNAL PROCESSOR
75
D
PR
CK
CLR
Q
G1
G2

FIG. 2


11
data, Gs, Vs
SOURCE DRIVER
Vcom
20
S1
S2
. . .
Sm
VIDEO DATA
POWER SUPPLY SIGNAL
SYNCHRONIZING SIGNALS
Hsync, and Vsync
VIDE SIGNAL PROCESSOR
Gs
, Vs
Vcom
GATE DRIVER
G1
G2
Gn
DISPLAY PANEL
10
50
BACKLIGHT DRIVER
BACKLIGHT
26
24

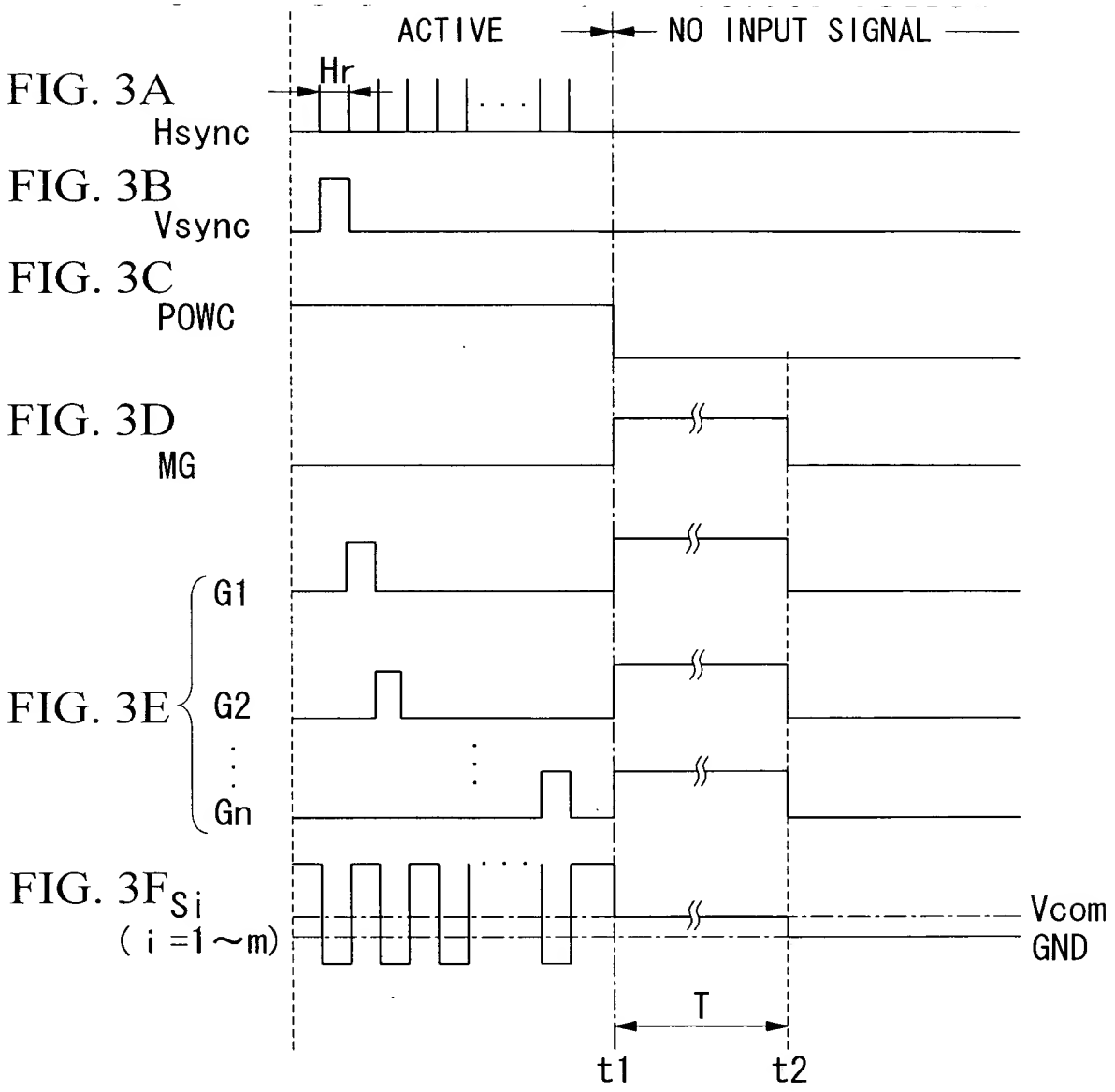

ACTIVE
NO INPUT SIGNAL
Hr
FIG. 3A
Hsync
FIG. 3B
Vsync
FIG. 3C
POWC
FIG. 3D
MG
FIG. 3E
G1
G2
Gn
FIG. 3F Si
(i =1~m)
Vcom
GND
T
t1
t2

FIG. 4

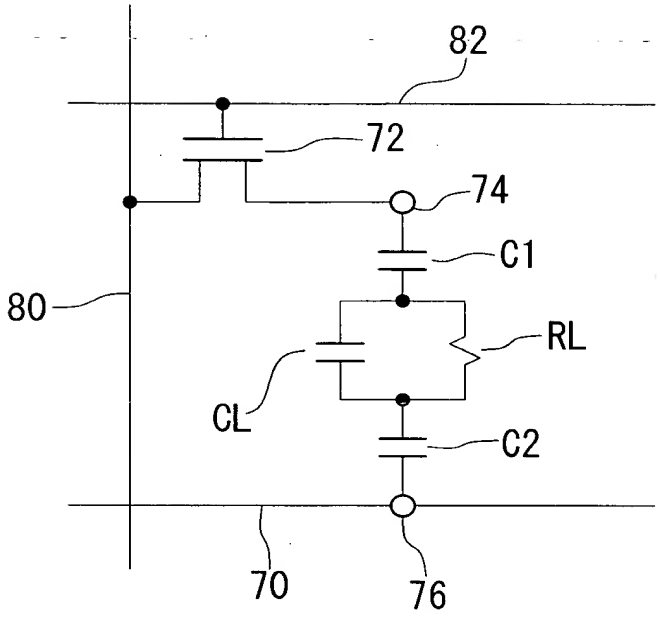

82
72
74
C1
80
RL
CL
C2
70
76

FIG. 5

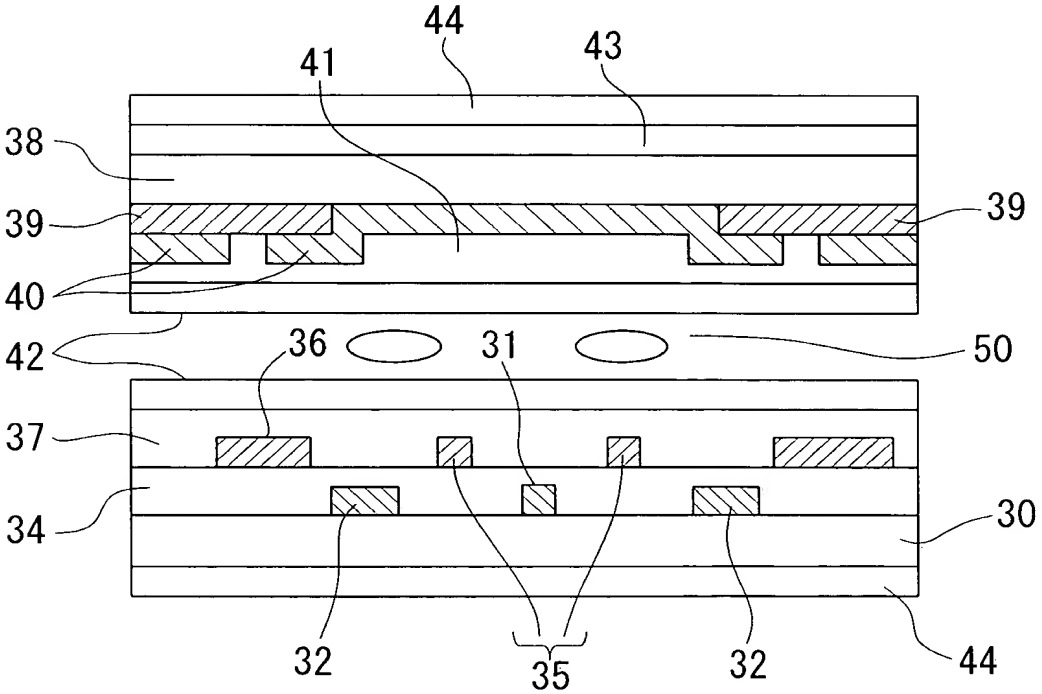

44
41
43
38
39
39
40
42
36
31
50
37
34
30
32
35
32
44

FIG. 6

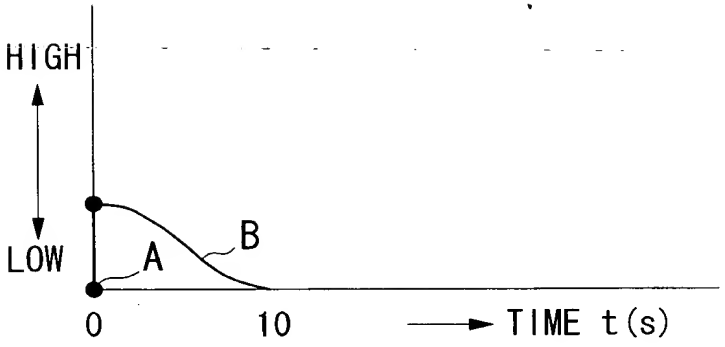


FIG. 7A

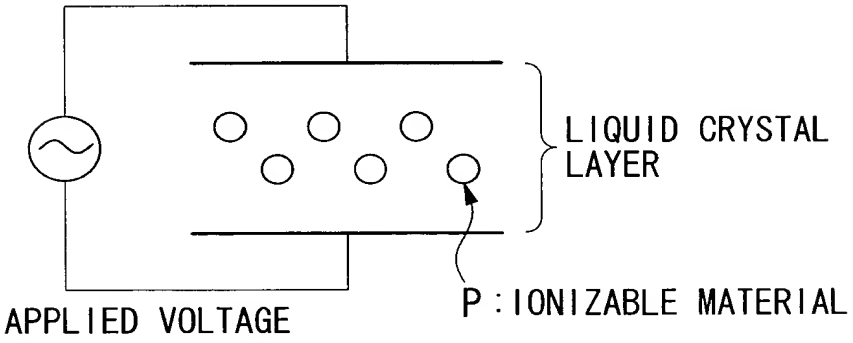


FIG. 7B